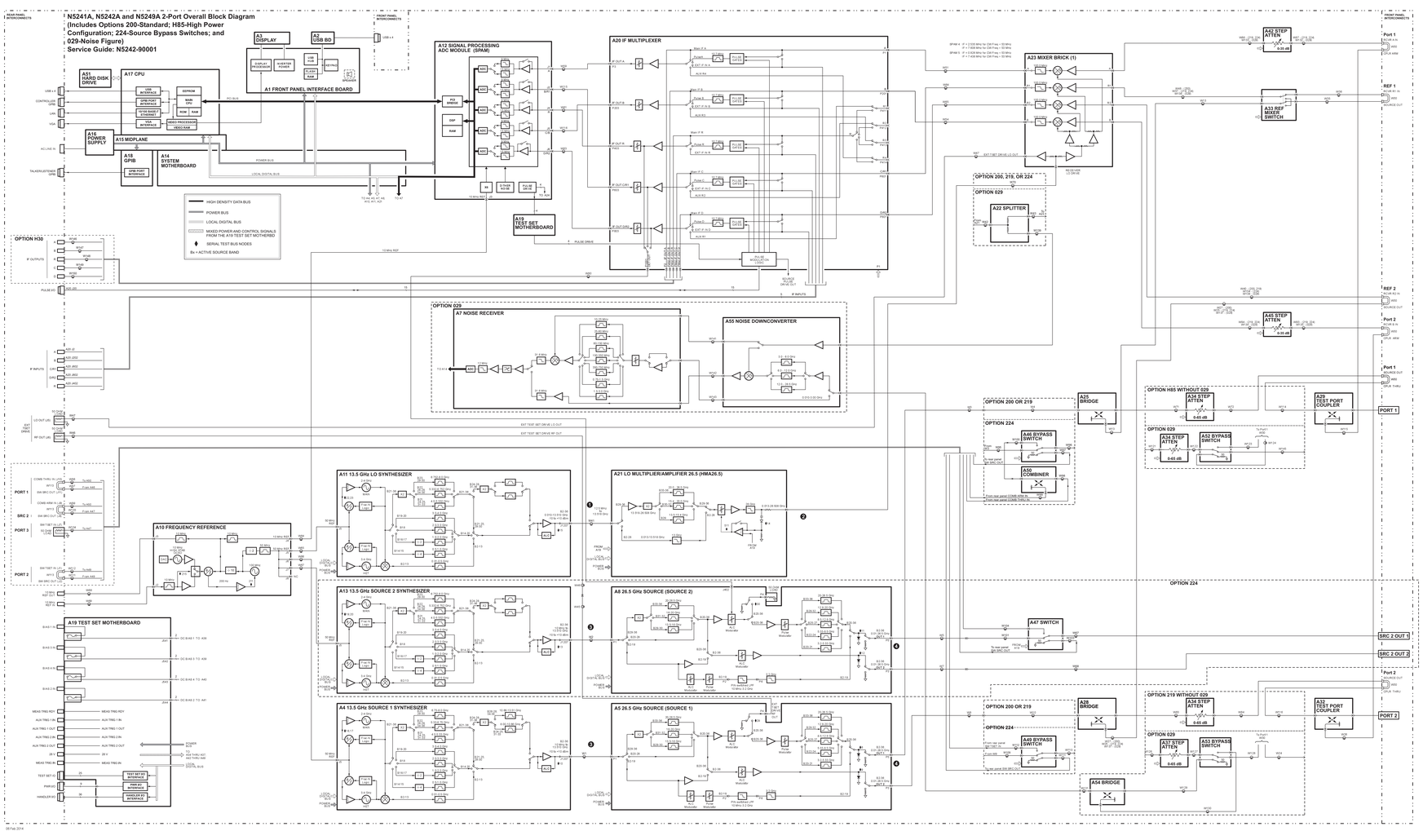
N5241A, N5242A and N5249A 2-Port Overall Block Diagram (Includes Options 200-Standard; H85-High Power Configuration; 224-Source Bypass Switches; and 029-Noise Figure) Service Guide: N5242-90001

Band	Mixer Brick L.O. Harmonic Number	❶ A11 Synthesizer Frequency (GHz)	❷ A21 MA26.5 Frequency (GHz)	❸ A4/A7 Synthesizer Frequency (GHz)	❹ A5/A8 Source Frequency (GHz)
0	-	-	-	-	-
1	-	-	-	-	-
2	1	0.01254 to 0.01654	0.01254 to 0.01654	0.010 to 0.014	0.010 to 0.014
3	1	0.01654 to 0.02154	0.01654 to 0.02154	0.014 to 0.019	0.014 to 0.019
4	1	0.02154 to 0.02954	0.02154 to 0.02954	0.019 to 0.027	0.019 to 0.027
5	1	0.02954 to 0.04054	0.02954 to 0.04054	0.027 to 0.038	0.027 to 0.038
6	1	0.04054 to 0.05554	0.04054 to 0.05554	0.038 to 0.053	0.038 to 0.053
7	1	0.06061 to 0.08261	0.06061 to 0.08261	0.053 to 0.075	0.053 to 0.075
8	1	0.08261 to 0.11261	0.08261 to 0.11261	0.075 to 0.105	0.075 to 0.105
9	1	0.11261 to 0.15361	0.11261 to 0.15361	0.105 to 0.146	0.105 to 0.146
10	1	0.15361 to 0.21261	0.15361 to 0.21261	0.146 to 0.205	0.146 to 0.205
11	1	0.21261 to 0.25761	0.21261 to 0.25761	0.205 to 0.250	0.205 to 0.250
12	1	0.25761 to 0.40361	0.25761 to 0.40361	0.250 to 0.396	0.250 to 0.396
13	1	0.40361 to 0.50761	0.40361 to 0.50761	0.396 to 0.500	0.396 to 0.500
14	1	0.50761 to 0.63561	0.50761 to 0.63561	0.500 to 0.628	0.500 to 0.628
15	1	0.63561 to 1.00761	0.63561 to 1.00761	0.628 to 1.000	0.628 to 1.000
16	1	1.00761 to 1.50761	1.00761 to 1.50761	1.000 to 1.500	1.000 to 1.500
17	1	1.50761 to 2.00761	1.50761 to 2.00761	1.500 to 2.000	1.500 to 2.000
18	1	2.00761 to 3.00761	2.00761 to 3.00761	2.000 to 3.000	2.000 to 3.000
19	1	3.00761 to 3.20761	3.00761 to 3.20761	3.000 to 3.200	3.000 to 3.200
20	1	3.20761 to 4.00761	3.20761 to 4.00761	3.200 to 4.000	3.200 to 4.000
21	1	4.00761 to 5.33961	4.00761 to 5.33961	4.000 to 5.332	4.000 to 5.332
22	1	5.33961 to 6.75961	5.33961 to 6.75961	5.332 to 6.752	5.332 to 6.752
23	1	6.75961 to 8.00761	6.75961 to 8.00761	6.752 to 8.000	6.752 to 8.000
24	1	8.00761 to 8.50761	8.00761 to 8.50761	8.000 to 8.500	8.000 to 8.500
25	1	8.50761 to 10.67161	8.50761 to 10.67161	8.500 to 10.664	8.500 to 10.664
26	1	10.67161 to 12.00761	10.67161 to 12.00761	10.664 to 12.000	10.664 to 12.000
27	1	12.00761 to 12.80761	12.00761 to 12.80761	12.000 to 12.800	12.000 to 12.800
28	1	12.80761 to 13.51761	12.80761 to 13.51761	12.800 to 13.510	12.800 to 13.510
29	1	6.75880 to 7.70380	13.51761 to 15.40761	6.755 to 7.700	13.510 to 15.400
30	1	7.70380 to 8.00380	15.40761 to 16.00761	7.700 to 8.000	15.400 to 16.000
31	1	8.00380 to 9.00380	16.00761 to 18.00761	8.000 to 9.000	16.000 to 18.000
32	1	9.00380 to 10.00380	18.00761 to 20.00761	9.000 to 10.000	18.000 to 20.000
33	1	10.00380 to 10.66761	20.00761 to 21.33561	10.000 to 10.664	20.000 to 21.328
34	1	10.66761 to 11.25361	21.33561 to 22.50761	10.664 to 11.250	21.328 to 22.500
35	1	11.25361 to 12.00361	22.50761 to 24.00761	11.250 to 12.000	22.500 to 24.000
36	1	12.00361 to 13.25361	24.00761 to 26.50761	12.000 to 13.250	24.000 to 26.500

Test Node	Error Description	Assembly	Frequency Band
8	Unleveled, Source 1, Out 1	A5	Full Range
9	Unleveled, Source 1, Out 2	A5	Full Range
10	Unleveled, Source 1 Synthesizer	A4	Full Range
11	Unleveled, Source 2, Out 1	A8	Full Range
12	Unleveled, Source 2, Out 2	A8	Full Range
13	Unleveled, Source 2 Synthesizer	A13	Full Range
14	Unleveled, LO Drive	A21	Full Range
15	Unleveled, LO Synthesizer	A11	Full Range
16	Unlocked, Source 1 Synthesizer, Integrator Low	A4	Full Range
17	Unlocked, Source 1 Synthesizer, Integrator High	A4	Full Range
19	Unlocked, Source 2 Synthesizer, Integrator Low	A13	Full Range
20	Unlocked, Source 2 Synthesizer, Integrator High	A13	Full Range
22	Unlocked, LO Synthesizer, Integrator Low	A11	Full Range
23	Unlocked, LO Synthesizer, Integrator High	A11	Full Range
29	Unleveled, Source 1, P4	A5	13.5 - 26.5 GHz
30	Unleveled, Source 2, P4	A8	13.5 - 26.5 GHz